



(19)

Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 1 513 202 A1

(12)

## EUROPEAN PATENT APPLICATION

(43) Date of publication:  
09.03.2005 Bulletin 2005/10

(51) Int Cl 7: H01L 31/0352, G01J 1/42

(21) Application number: 03077744.5

(22) Date of filing: 02.09.2003

(84) Designated Contracting States:  
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR  
HU IE IT LI LU MC NL PT RO SE SI SK TR  
Designated Extension States:  
AL LT LV MK

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## Remarks:

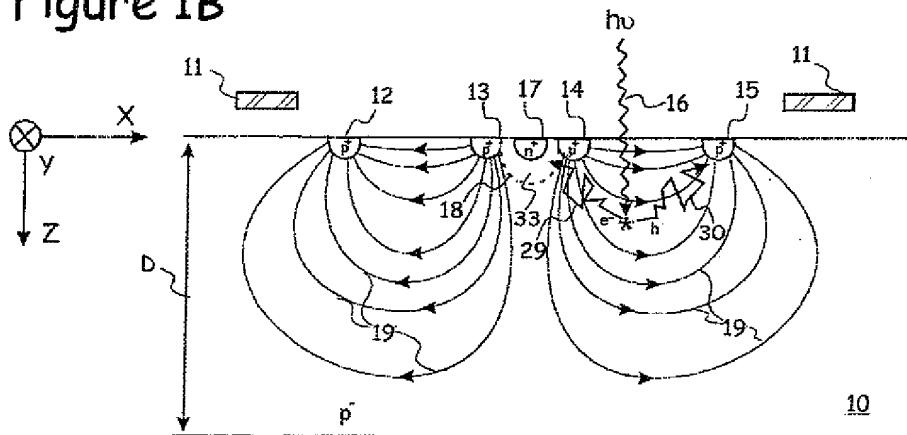
Amended claims in accordance with Rule 86 (2)  
EPC.

## (54) Detector for electromagnetic radiation assisted by majority current

(57) The present invention relates to a novel photo-detector structure and method whereby light is detected in a semiconductor substrate. Through the use of a small current of majority carriers an electrical field is created that directs photo-generated minority carriers towards a detection region. In this way a large sensitive area 34 for a detector can be designed with a detection region having low junction area and low associated detector capacitance, thus enhancing output signal to noise ratio and/or speed of attached readout circuits. Simultaneously the assistant majority current can sweep

the minority carriers from deep within the substrate towards the detection region thus improving the overall detector sensitivity. By making the majority current reconfigurable a photonic mixer structure with two or more detection regions can be conceived whereby the direction of the applied current will determine in an efficient way to which detection region the photo-generated minority carriers will be directed. In this way, incident modulated light can get demodulated, such that phase and amplitude of the modulated light can be measured for a.o. range finding applications.

Figure 1B



**Description****Field of the invention**

[0001] The present invention relates to the field of detection of electromagnetic radiation that generates minority carriers in a semi-conductor substrate such as for example, but not limited to detection of visible and infrared light. More particularly, the present invention relates to electromagnetic radiation detection in a substrate and more specifically on the application of a majority current for directing photo-generated minority charges towards a detection region as well as an apparatus therefor.

**Technical Background of the invention**

[0002] US-6157035 describes how a fast detector in a standard CMOS technology can be constructed. A shadow mask gives a spatial modulation to the incident and consequently of the absorbed light in the semiconductor substrate. By measuring the magnitude of the spatial frequency component in the minority carrier distribution with a spatial frequency corresponding to that of the shadow mask, a fast detector is provided. Speed is hereby enhanced but the traded-off is sensitivity.

[0003] WO 98/10255 and WO 99/60629 show methods and corresponding devices for determining the phase and/or amplitude of incident modulated light. These pixel structures are called photonic mixers. These structures are useful for making range-finding cameras giving depth or 3D information as is explained in these patent applications. By applying a modulation voltage over two photo-gates, the generated minority carriers in the substrate arrive preferentially at one of two detector accumulation zones. In WO 98/10255, these accumulation zones are potential wells, created by a voltage on an adjacent accumulation gate. In WO 99/60629, these accumulation zones are pn-junctions. There are however two major deficiencies with these photonic mixers. Firstly for light that is absorbed more deeply in the substrate the generated minority carriers will have to diffuse towards the surface before the modulation photo-gates can redirect these carriers towards the destination accumulation zones. This diffusion process over a 5 to 20  $\mu\text{m}$  distance limits the speed with which demodulation can be achieved. Second, the percentage of detected minority carriers that reach effectively the target accumulation zone is far from optimal.

**Summary of the invention**

[0004] The present invention aims to provide a novel photo-detector structure that has a pn-junction detection area that can be of an order of magnitude smaller in area than the effective light sensitive area thereby lowering considerably the capacitance of the detecting pn-junction. Further it is also an aim of the invention to provide

a method of collecting photo-generated carriers that are present deeply in the substrate in a fast way in order to obtain a fast and sensitive detector.

[0005] Another aim of the invention is to provide a photonic mixer structure and method that has two or more detector outputs, and whereby the photo-generated minority carriers get collected at a target detector output. An aim hereby is to reach this in a very efficient way with high discrimination- and quantum efficiency. High detection bandwidth and high speed of switching between specified detector outputs are aimed for as well.

[0006] The above objectives are accomplished by a method and device according to the present invention.

[0007] In one aspect, the present invention provides a detector device for detection of electromagnetic radiation impinging on a substrate, the impinging electromagnetic radiation generating pairs of majority and minority carriers in the substrate. The detector device comprises means for generating in the substrate, a majority carrier current, and at least one detection region for collecting generated minority carriers the minority carriers being directed towards the at least one detection region under influence of the majority carrier current.

[0008] The detector device may furthermore comprise at least one current source region and at least one current drain region the means for generating the majority carrier current being adapted for generating this majority carrier current between the at least one current source region and the at least one current drain region.

[0009] The at least one current source region may be located between the at least one current drain region and the at least one detection region. Alternatively, the at least one detection region may be located between the at least one current source region and the at least one current drain region.

[0010] The current source region the current drain region and the detection region may be located substantially in the same plane. With "substantially in the same plane" is meant that they for example have a top surface in the same plane as a plane is two-dimensional and per definition has no thickness, while the current source region the current drain region and the detection region are three-dimensional structures. The current source region, current drain region and detection region may be located at the surface of the substrate of the detector device.

[0011] The current source region may have an annular shape in the plane of the substrate. Also the current drain region may have an annular shape in the plane of the substrate.

[0012] A detector device according to the present invention may furthermore comprise readout means for reading out the minority carriers collected in the detection region. The readout means may comprise a transimpedance amplifier

[0013] In a detector device according to the present invention, minority carriers having reached the at least one current source region may travel towards the at least one detection region by diffusion.

[0012] The sensitive area of a detector according to the present invention may be substantially larger than the area of at least one detection region.

[0013] A detector device according to the present invention may be a photonic mixer. The means for generating the majority carrier current may be reconfigurable, so as to be able to generate the majority carrier current in either at least a first direction or a second direction different from the first direction. The second direction may be opposite to the first direction.

[0014] In a second aspect, the present invention provides the use of a detector according to the present invention in a time of flight range-finding application.

[0015] In a third aspect, the present invention provides a method for detection of electromagnetic radiation impinging on a substrate, the impinging electromagnetic radiation generating pairs of majority and minority carriers in the substrate. The method comprises generating a majority carrier current in the substrate, and directing generated minority carriers towards a detection region under influence of the generated majority carrier current.

[0016] The method may furthermore comprise collecting the minority carriers in the detection region, and reading out the minority carriers collected in the detection region.

[0017] A method according to the present invention may furthermore comprise generating the majority carrier current so as to at least first generate the majority carrier current in a first direction and to direct generated minority carriers towards a first detection region, and thereafter generate the majority carrier current in a second direction, the second direction being different with respect to the first direction, to thereby direct generated minority carriers towards a second detection region. The second direction may be opposite to the first direction.

[0018] These and other characteristics, features and advantages of the present invention will become apparent from the following detailed description taken in conjunction with the accompanying drawings, which illustrate by way of example, the principles of the invention. This description is given for the sake of example only, without limiting the scope of the invention. The reference figures quoted below refer to the attached drawings.

#### Short description of the drawings

[0019]

Fig. 1A shows a top view of a photo-detector according to a first embodiment of the present invention.

Fig. 1B shows a cross-section along line I-I' of the detector illustrated in Fig. 1A.

Fig. 2A shows a simplified diagrammatic sketch of the top-view of the photo-detector of Fig. 1A.

Figs. 2B, 2C, 2D and 2E show alternative embodiments, namely second to fifth embodiments, of the

present invention using the simplified representation principle as in Fig. 2A.

Fig. 3A shows a top view of an embodiment of a photonic mixer structure whereby an applied majority current determines that photo-generated electrons are directed towards a detecting pn-junction. Fig. 3B shows a cross-section of the photonic mixer structure of Fig. 3A in the same current condition. Fig. 3C shows a cross-section of the photonic mixer structure of Fig. 3A, whereby the majority current is inverted, thereby determining that photo-generated minority carriers get directed towards another detecting pn-junction.

Fig. 4A and 4B show simplified sketches of the top-view of the photonic mixer structure of Fig. 3A, with the majority current conditions of Fig. 3A and Fig. 3C respectively.

Fig. 4C and 4D show an alternative embodiment of a photonic mixer structure using the simplified representation as in Figs. 4A and 4B.

Fig. 4E shows still another embodiment of a photonic mixer structure having four majority current conditions that can be applied whereby for each condition the electrons are directed towards a separate mixer output connection. Only one current condition and biasing level are shown.

[0020] In the different figures the same reference figures refer to the same or analogous elements.

#### Detailed description of the invention

[0021] The present invention will be described with respect to particular embodiments and with reference to certain drawings but the invention is not limited thereto but only by the claims. The drawings described are only schematic and are non-limiting. In the drawings the size of some of the elements may be exaggerated and not drawn on scale for illustrative purposes. The dimensions and the relative dimensions do not correspond to actual reductions to practice of the invention.

[0022] It is to be noticed that the term "comprising", used in the claims, should not be interpreted as being restricted to the means listed thereafter. Thus, the scope of the expression "a device comprising means A and B" should not be limited to devices consisting only of components A and B. It means that with respect to the present invention, the most relevant components of the device are A and B.

[0023] Similarly, it is to be noticed that the term "coupled" should not be interpreted as being restricted to direct connections only. Thus, the scope of the expression "a device A coupled to a device B" should not be limited to devices or systems wherein an output of device A is directly connected to an input of device B. It means that there exists a path between an output of A and an input of B which may be a path including other devices or means

[0024] Furthermore, the terms first, second, third and the like in the description and in the claims are used for distinguishing between similar elements and not necessarily for describing a sequential or chronological order. It is to be understood that the terms so used are interchangeable under appropriate circumstances and that the embodiments of the invention described herein are capable of operation in other sequences than described or illustrated herein.

[0025] The invention will be described by a detailed description of several embodiments of the invention. It is clear that other embodiments of the invention can be configured according to the knowledge of persons skilled in the art without departing from the true spirit or technical teaching of the invention, the invention being limited only by the terms of the appended claims. It will be clear for a person skilled in the art that the present invention is also applicable to similar devices that can be configured in any transistor technology, including for example, but not limited thereto, CMOS, BiCMOS, Bipolar and SiGe BiCMOS technology.

[0026] Furthermore the findings of the present invention are explained with reference to a p-substrate as an example, but the present invention includes within its scope a complementary device whereby p and n-regions become n and p-regions, respectively. A skilled person can make the such modifications without departing from the true spirit of the invention.

[0027] Fig. 1A shows a top view of a first embodiment of a photo-detector according to the present invention. Sources 25 and 26 inject current at contacts 21 and 23 respectively, and drain current at contacts 20 and 24 respectively. Contacts 20, 21, 23 and 24 connect through ohmic-conduction to highly doped regions having a first conductivity type e.g. p<sup>+</sup>-regions 12, 13, 14 and 15 respectively. In a resistive, lightly doped substrate having the first conductivity type e.g. p-substrate 10, a majority carrier current, in the case of a substrate being a p<sup>-</sup>substrate a hole current 19 will flow from region 13 to region 12 and from region 14 to region 15. Line I-I' in Fig. 1A shows where the cross-section is made for Fig. 1B. In Fig. 1B, the majority hole current 19 is shown by solid lines whereby the direction of the hole current 19 is indicated by arrows. In the substrate 10, where the doping is typically considered to be constant the majority current 19 is associated with an electrical field, in the direction of the current. If incident electromagnetic radiation such as light in the form of a photon 16 is incident within a sensitive area 34 of the detector, the energy associated with the photon 16 is absorbed by the substrate 10, and a reaction takes place that generates an electron hole pair, at a certain position. In the example illustrated in Fig. 1B the photon 16 falls into the right part of the sensitive area 34. In Fig. 1B as an example, the electron hole pair generation is considered to occur at the location indicated with a star: \*'. The electron-hole pair e<sup>-</sup>/h<sup>+</sup> will be separated by the electrical field that is present and which is associated to the flowing majority hole cur-

rent 19. The majority carrier of the electron-hole pair e<sup>-</sup>/h<sup>+</sup>, in the present example the hole h<sup>+</sup>, will become indistinguishable from the sea of majority carriers holes in this example, of the flowing majority hole current 19

5 and will follow for example trajectory 30. The minority carrier of the electron-hole pair, namely in the example given the electron e<sup>-</sup> on the contrary, will move in the opposite direction (due to its negative charge), driven by the same electrical field associated with the flowing majority current 19 and undergoes collisions with phonons generating an erratic trajectory, like trajectory 29. This erratic trajectory 29 has however a general component that is opposite to the majority current flow 19 of the holes directing the electron e<sup>-</sup> towards the p<sup>+</sup>-region 14. The general component in the movement is due to drift based on the present electric field associated with the flowing majority current 19 and the erratic behaviour is due to diffusion, based on collisions with phonons Very close to region 14, a detecting pn-junction 18 is provided. The minority carrier electron e<sup>-</sup> being directed towards region 14, will have a large chance to reach the depletion zone of this junction 18 and to be detected due to its erratic behaviour (or diffusivity).

[0028] 10 It will then form part of the photocurrent that is brought out of the detector through the highly conductive region (i.e. highly doped region) having a second conductivity type, e.g. n<sup>+</sup>-region 17, through contact 22, connected to readout circuit 27. Readout circuit 27 is typically a transimpedance amplifier, and will typically 15 be integrated on the same substrate 10 as the detector thus avoiding packaging parasitics. In order to keep the junction capacitance per unit area low, the detecting pn-junction 18 is preferably located between a lowly doped well of the second conductivity type, n-well 33, and the substrate of the first conductivity type, p-substrate 10. Inside the n-well 33, preferably an n<sup>+</sup>-region 17 is added for making the detector finger formed by n<sup>+</sup>-region 17 and n-well 33 more conductive. As the junction capacitance per unit area is kept low, circuits attached to the 20 photo-detector and optimised for this photo-detector with lower capacitance can reach a considerable better signal to noise output ratio and/or improved speed characteristics.

[0029] 25 The n-well 33 should be located close to the hole current sources 13 and 14 in order to increase the chance and the speed with which the electrons will diffuse into the n-well 33 through the detecting pn-junction 18, and thus to increase the sensitivity of the detector. Ohmic contacts in general serve as effective recombination centres for minority carriers so ohmic contacts are preferably only present very locally, e.g. in a corner of p<sup>+</sup>-regions 13 and 14 like ohmic contacts 21 and 23 as illustrated in Fig. 1A. If such ohmic contact would be present over a substantial part of these p<sup>+</sup>-regions 13 and 14 the minority carriers electrons in the example given, would likely be recombining at these extended ohmic contacts instead of diffusing towards the detecting pn-junction 18.

[0030] In short, incident photons like photon 16 get absorbed in the substrate 10. The electrical field associated with the flowing majority current 19 will cause the photo-generated minority carriers (being electrons in the example given) to drift in the opposite direction to the flowing majority current 19, i.e. towards the source regions 14 and 13 of this majority current 19. By diffusion finally the electrons arrive in the n<sup>+</sup>-region 17 and form the detector current going to the readout circuit 27. Beside photons like the example photon 16 a small fraction of the incident photons will be directly absorbed in the detecting finger 17 (region of n-well 33) not requiring the electric field associated to the majority current 19 for transportation.

[0031] In this way, the region sensitive to incident photons becomes as large as region 34 illustrated in Fig. 1A and Fig. 1B, whilst the detecting finger 17 (region of n-well 33) has only a small capacitance due to its limited finger area. This can be an order of magnitude smaller than when the full sensitive area 34 would be covered with a pn-junction. This effect has similar benefits to focusing a wide incident light beam through a lens on a small area photo-detector. Hence one can name the present invention method and setup an "electronic lens" for a photodetector. The large sensitive area 34 together with the small area pn-junction 18 for detection of the minority carriers effectively enhances the reachable signal to noise ratio. Further, having a larger sensitive area 34, the alignment of light input means to the sensitive area will require less precision lowering fabrication cost or connector precision (in the case of repeatable connections).

[0032] The speed with which the electrons reach the detecting finger 17 depends on the magnitude of the electrical field that is associated with the majority hole current 19. A person skilled in the art can use device simulators (like ATLAS from Silvaco or MEDICI from Synopsys) and other calculation means or trial and error to determine which field has to be applied to reach a certain speed. In general the higher the voltage of sources 25 and 26 are the higher the speed will be. A voltage of 2V gives for a trajectory distance of 20 µm a delay of about 5 ns for movement of minority electrons in a silicon substrate 10. With a voltage of 2V and a 40 µm x 40 µm sensitive detector area, a speed of over 100 MHz can be obtained in standard CMOS. The required majority current 19 depends on the area, the distance between, and the width and length of regions 12, 13, 14 & 15. With typical CMOS-substrates 10 with a resistivity of 20 ohm.cm, and the aforementioned area and voltage an assistant current of about 400 µA would flow with a 2 volt applied voltage at the sources 25 and 26. With high resistive substrates 10 this assistant current can become very small. High resistive silicon substrates having a resistivity of 1000 ohm.cm are nowadays not uncommon, lowering the current down to even 8 pA at roughly the same speed (since the electrical field distribution would remain more or less the same).

[0033] Further when the majority current sourcing finger 13 and the majority current sinking finger 12 are spaced apart by over 20 µm, this being the distance in the X-direction as shown in Fig. 1A, the depth at which carriers still experience a reasonable field due to the majority current 19 is at about a 20 µm deep level D (in the Z direction - see Fig. 1B) from the top of the substrate 10. This increases the overall quantum efficiency of the detector, especially in cases where light is penetrating deeply in the substrate like with light having a wavelength of 860 nm. In that case electron-hole pair generation also occurs a depth of 20 microns. Standard CMOS technology typically only offers shallow junctions up to 2 microns deep. Light absorbed at 20 microns is difficult to capture for the junctions, since the generated minority carriers first have to diffuse to this shallow junction before getting detected and becoming part of a photo-current through the terminals of the detector.

[0034] In accordance with an aspect of the present invention the readout circuit 27 may comprise a transimpedance amplifier. A transimpedance amplifier typically comprises an opamp 31 and a feedback resistor 32. It is used to convert an input current to an output voltage. For a given bandwidth, the transimpedance amplifier 31, 32 can be designed to have about a 10 times higher transimpedance gain, when supposing that a ten times lower detector capacitance becomes feasible. In this way, the overall sensitivity (i.e. the output voltage change at output node 28 of readout circuit 27 per unit incident light) increases up to a factor of 10. The signal to noise ratio improves as well by a factor of 3 to 10, depending on whether the noise is mainly due to the noise of the input transistors in the transimpedance amplifier's amplifier 31 or due to the transimpedance amplifier's feedback resistor 32.

[0035] Electromagnetic masking such as metal regions 11 can be used to prevent that the impinging electromagnetic radiation, such as light, penetrates in unwanted areas. Area 11 in Fig. 1A is an example of such an area.

[0036] Preferably care should further be taken, to avoid that the detecting pn-junction 18 does not have forward (or too much forward) bias by the application of the voltage on the adjacent majority current source regions 13 and 14.

[0037] Figs. 2A to 2E show several embodiments of devices according to the present invention. Fig. 2A shows a simplified diagrammatic top-view representation of the detector represented in Fig. 1A, whereby only the necessary subset of elements is represented. The thick solid lines represent the regions of the first conductivity type p<sup>+</sup>-regions 12, 13, 14, 15, for applying the majority current. The thick dashed line represents the detector finger 17 in n-well region 33. The voltages applied by sources 25-26 are also indicated. The voltage on the detector finger 17 is "V<sub>det</sub>". This voltage depends on how the transimpedance amplifier 31 is biased. Typically, this voltage is halfway the power supply voltage.

For a power supply voltage of 5V,  $V_{det}$  is thus typically 2.5V.

[0038] Fig. 2B shows a second embodiment of a detector according to the present invention. It comprises only one source region 40 for injection of the majority current 19, with at each side of it a detector finger 41, 42. In the embodiment shown the source region 40 is located in the middle of the device. The detector fingers 41, 42 should be connected in parallel. This set-up enhances the speed of the diffusion part of the detecting process, but it also doubles the detector capacitance since the number of detector fingers 41, 42 is doubled.

[0039] Fig. 2C and 2D illustrate detectors of a rectangular type.

[0040] Especially the embodiment of Fig. 2C is considered a preferred arrangement or best mode example, since in this embodiment the detector capacitance becomes extremely low. Instead of using a detector finger 17 which for example has a dimension of 40  $\mu\text{m} \times 3 \mu\text{m}$  there is a detecting pn-junction 44 with an area that can be as small as of the order of only 3  $\mu\text{m} \times 3 \mu\text{m}$  whilst the sensitive light input area is still 50  $\mu\text{m} \times 50 \mu\text{m}$ . A current source region 43, which may be annular with a rectangular shape in top view as illustrated in Fig. 2C, surrounds the detecting pn-junction 44. Current drain regions 45 are spaced apart for the current source region 43. The current drain regions 45 may also form an annular region with a rectangular shape in top view as illustrated in Fig. 2C. Alternatively, not shown in the drawings, the current drain regions 45 do not need to touch each other, such that separated current drain regions 45 may be provided. In the same way, separated current source regions 43 may be provided around the detecting pn-junction 44.

[0041] The detector of the third embodiment illustrated in Fig. 2D has in the centre its current source region 43, surrounded by a detecting pn-junction 44. Here the detector capacitance is somewhat higher since the area of the detecting pn-junction 44 is somewhat larger, but it remains low. This set-up enhances the speed of the diffusion part of the detecting process, since the current source region 43 is surrounded by the detecting pn-junction 44.

[0042] Fig. 2E shows a fifth embodiment of a detector according to the present invention, in which the minority carriers e.g. electrons, are not driven to the centre, but to the edge of the detector. In the middle the current drain region 45 can be biased at a low negative voltage, of e.g. -10V, without hindering the functioning of circuits outside the detector. This is preferred for higher speeds in the GHz range. The outside region(s) of the first conductivity type, e.g. a p<sup>+</sup>-ring 43 as in Fig. 2E or separated p<sup>+</sup>-regions, that sources the majority hole current 19 can be biased at a voltage higher than the negative voltage of the current drain region 45 at a high voltage, e.g. at 0V or 2V. With these stronger applied majority currents 19 and hence stronger electrical fields, up to GHz speeds can be obtained, still with a reasonably small

detector capacitance

[0043] The structure of the device illustrated in Fig. 3 makes use of the same principles as the detector of Fig. 1 however, it has another function. One of the uses is in "time of flight" (TOF) range-finding applications. Prior art documents WO 98/10255 and WO 99/60629 explain the general principle of time of flight measurements for range finding applications. A light source is modulated at a frequency in the range of 1MHz to 1GHz. The light illuminates an object, or scene, and part of the reflected light enters the range finder camera through a focused lens. By measuring in each pixel the phase of the incident light a distance can be estimated between the pixel and its conjugate (light-reflecting) pixel-area in the scene. In this way the distances of objects and the shape of objects can be estimated and recorded.

[0044] Technical problem in the known type of TOF range finders is to find a way to measure the phase of the reflected light accurately. The phase accuracy is linked to the precision of estimating the distance. The better the signal to noise ratio, the less noise, and thus error there is on the estimated distance. For that purpose, it can be calculated that low noise can be achieved by mixing the incident light right away in the detector instead of in a connected electronic mixer. An electronic mixer is an inherent source of noise due to the transistors that have to operate at high frequency and at high bias currents and should hence be avoided.

[0045] Fig. 3A illustrates a photonic mixer according to an embodiment of the present invention, that mixes incident amplitude modulated electromagnetic radiation with an electrical signal applied to the substrate 10 through source 25 and outputs two electrical photo-currents at output nodes Mix1 and Mix2. Fig. 3B shows a cross-section through line III-III' of the device illustrated in Fig. 3A. The electrical signal applied by source 25, generates a majority current, e.g. majority hole current 19, through the substrate 10. Minority carriers, like electrons in a p-substrate 10, will feel the electrical field that is associated with the applied majority hole current 19, and will drift towards a first source of majority carriers, e.g. holes, which is in the example illustrated p<sup>+</sup>-region 61. They will then diffuse into an adjacent first detection region, n-well 67, and they will become part of the output photocurrent of the left mixer connection point Mix1. A possible electron trajectory is trajectory 66, as illustrated in Fig. 3B.

[0046] When the applied voltage or electrical signal is inverted as illustrated in Fig. 3C, the direction of the majority current flow is inverted, and the minority carriers drift towards the complementary source region, p<sup>+</sup>-finger 62. Thereafter, most of the minority carriers namely electrons diffuse into the second detection region, n-well 68, becoming part of the output photocurrent of the right mixer connection point MIX2. A possible electron trajectory is trajectory 69, as shown in Fig. 3C.

[0047] Electromagnetic masking such as metal areas 60, can be used to block unwanted light which otherwise

would be directly incident on the detecting regions 67, 68.

[0048] This photonic mixer has a high efficiency. First of all, the electrical field associated with the majority current 19, that cause the drift of the minority carriers namely electrons, in the specified direction can penetrate deep in the substrate 10. How deep depends largely on the distance in X direction, i.e. parallel with the surface of the substrate 10 between p<sup>+</sup>-regions 61 and 62 as with the aforementioned detector of the present invention. The larger the pixel, i.e. the larger the distance between the p<sup>+</sup>-regions 61 and 62, the deeper the majority current 19 and hence the deeper the associated electrical fields penetrate. A distance of 20 µm between regions 61 and 62 will accommodate light that penetrates about 20 µm deep. This is useful for infrared light with a wavelength of 860 nm in Silicon. A switching speed and delay of electron transit in the order of nanoseconds is sufficient for many range-finding applications. When the majority current 19 penetrates deep enough for a given wavelength, it can also be expected that the quantum efficiency and the discrimination efficiency are near to optimal.

[0049] Figs. 4A and 4B are simplified, schematic representations of the photonic mixer illustrated in Fig. 3A for the two majority current conditions of Fig. 3B and 3C. The thick solid lines represent the p<sup>+</sup>-regions 61, 62 for applying the majority current 19. Thick dashed lines represent detector fingers 63, 64 in detection regions 67, 68. An example of applied voltages is also indicated.

[0050] Figs. 4C and 4D represent a second embodiment of a photonic mixer according to the present invention, under two different majority current conditions. This mixer has further a current drain region, e.g. formed by a p<sup>+</sup>-ring or a plurality of separated p<sup>+</sup>-regions (not represented in the drawings), that surrounds the sensitive area 34, and that is always connected to a low voltage, e.g. GND (0V). Further there are two short fingers 72, 73 of the first conductivity type, e.g. p<sup>+</sup>-fingers, that are either connected to a high voltage, e.g. 2V, or disconnected (floating). In Fig 4C the left short finger 72 is connected to the high voltage, e.g. 2V, and it sources current into the substrate 10, generating current lines of a majority current 19 to said outer p<sup>+</sup>-ring 71 connected to the low voltage, e.g. ground. Minority carriers, e.g. electrons, are driven by the field associated to these current lines 19 towards the short finger 72 at the high voltage, e.g. 2V. Then they are diffused to a first detection finger Mix1. The other short p<sup>+</sup>-finger 73 is left floating (High Impedance = Hi-Z). When the high voltage is applied to the short p<sup>+</sup>-finger 73 at the right and the short p<sup>+</sup>-finger 72 at the left is left floating, the minority carriers, electrons, get targeted to the detection finger Mix2 at the right.

[0051] For doing the phase estimation one would typically apply an electrical oscillating signal source 25 to the photonic mixer that is in phase, and thereafter (or in a second photonic mixer), one that is 90° delayed in

phase with respect to the phase of the oscillating light source. For measuring the mixing products one would typically first reset the terminals Mix1 and Mix2 to a high voltage e.g. 5 Volt, and then apply the oscillating source

5 to the mixer, and after a measuring accumulation period stop this oscillation for measuring what the integrated voltage drops on Mix1 and Mix2 have become. These voltage drops are then a result of the accumulation of the photo-current on nodes Mix1 and Mix2. The common mode voltage drop will then give an indication of the DC incident component of the light, whilst the difference in voltage between Mix1 and Mix2 gives the part of the mixing product that is used for measuring the effective phase and/or distance. WO 98/10255 and WO  
10 99/60629 are incorporated herein by reference and can be consulted to explain the concepts of TOF, phase and amplitude measurements in further detail for range finding applications.

[0052] Further for some measurement set-ups it can be useful to design a photonic mixer that has more than two mixer output terminals. The number of configurable current conditions has then to be adapted to the number of mixer output terminals. Person skilled in the art can easily extend the findings of this patent to this purpose

25 [0053] Fig. 4E shows such a photonic mixer, having four current situations and four mixer outputs. More precise phase detection can be expected with this structure, since, 0, 90, 180 and 270 degrees mixing can be achieved during a same measuring accumulation period and on the same sensitive detector area. For this to work well, subsequently it is preferred if each p<sup>+</sup>-region is used as current inlet, e.g. by biasing it at a high voltage of e.g. 2V, whilst the other 3 p<sup>+</sup>-regions are kept at a low voltage of e.g. 0V. In that way, the direction of the majority current 19 in the middle of the mixer can be made to rotate in order to fulfil its mixing purpose.

[0054] Furthermore, a person skilled in the art can also design other useful current configurations different from the one in Fig. 4E, without departing from the scope 30 of the present invention. For example, a current condition in which two or more p<sup>+</sup>-regions are simultaneously used as current inlet may be considered.

[0055] When a large photo-detector or photonic mixer would be required for some application the person skilled in the art can consider placing two or more detectors or photonic mixers according to embodiments of the present invention in parallel, in that way increasing the sensitive area to up to what is required.

[0056] It is to be understood that although preferred 35 embodiments, specific constructions and configurations as well as materials, have been discussed herein for devices according to the present invention various changes or modifications in form and detail may be made without departing from the scope and spirit of this invention

**Claims**

1. A detector device for detection of electromagnetic radiation impinging on a substrate (10), the impinging electromagnetic radiation generating pairs of majority ( $h^+$ ) and minority carriers ( $e^-$ ) in the substrate (10), the detector device comprising means (25, 26) for generating, in the substrate (10) a majority carrier current (19) and at least one detection region (17) for collecting generated minority carriers ( $e^-$ ), the minority carriers ( $e^-$ ) being directed towards the at least one detection region (17) under influence of the majority carrier current (19).

2. A detector device according to claim 1 furthermore comprising at least one current source region (13, 14) and at least one current drain region (12, 15) the means for generating the majority carrier current (19) being adapted for generating this majority carrier current between the at least one current source region (13, 14) and the at least one current drain region (12, 15).

3. A detector device according to claim 2, wherein the at least one current source region (13, 14) is located between the at least one current drain region (12, 15) and the at least one detection region (17).

4. A detector device according to claim 2 wherein the at least one detection region (41, 42) is located between the at least one current source region (40) and the at least one current drain region (12, 15).

5. A detector device according to any of claims 2 to 4 wherein the current source region (43) has an annular shape in the plane of the substrate (10).

6. A detector device according to any of claims 2 to 5, wherein the current drain region (45) has an annular shape in the plane of the substrate (10).

7. A detector device according to any of the previous claims, furthermore comprising readout means (27) for reading out the minority carriers ( $e^-$ ) collected in the detection region (17).

8. A detector device according to claim 7, wherein the readout means (27) comprises a transimpedance amplifier (31).

9. A detector device according to any of claims 2 to 8, whereby minority carriers ( $e^-$ ) having reached the at least one current source region (13, 14) travel towards the at least one detection region (17) by diffusion.

10. A detector device according to any of the previous claims whereby the sensitive area (34) is substantially larger than the area of the at least one detection region (17).

11. A detector device according to any of the previous claims, wherein the detector device is a photonic mixer.

12. A detector device according to claim 11 wherein the means (25, 26) for generating the majority carrier current is reconfigurable, so as to be able to generate the majority carrier current (19) in either at least a first direction or a second direction different from the first direction

13. A detector device according to claim 12, wherein the second direction is opposite to the first direction.

14. A detector device according to any of claims 2 to 13, wherein the current source region (13, 14) the current drain region (12, 15) and the detection region (17) are located substantially in the same plane

15. Use of a detector according to any of claims 11 to 13 in a time of flight range-finding application.

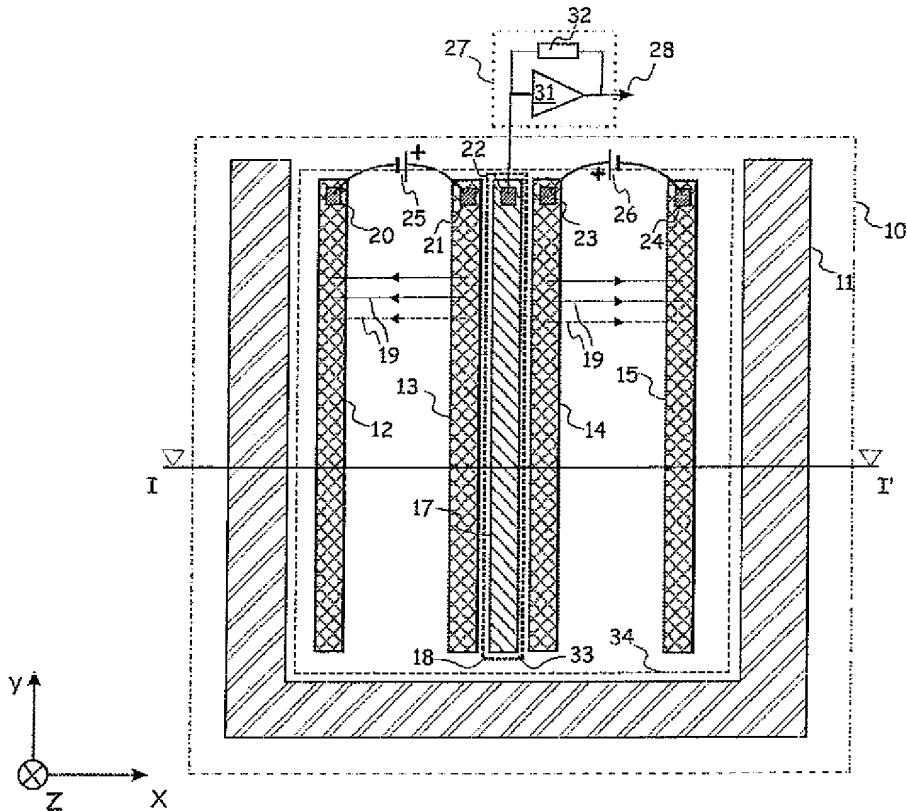
16. A method for detection of electromagnetic radiation impinging on a substrate (10), the impinging electromagnetic radiation generating pairs of majority ( $h^+$ ) and minority carriers ( $e^-$ ) in the substrate (10) the method comprising generating a majority carrier current (19) in the substrate (10), and directing generated minority carriers towards a detection region (17) under influence of the generated majority carrier current (19).

17. A method according to claim 16, furthermore comprising collecting the minority carriers ( $e^-$ ) in the detection region (17).

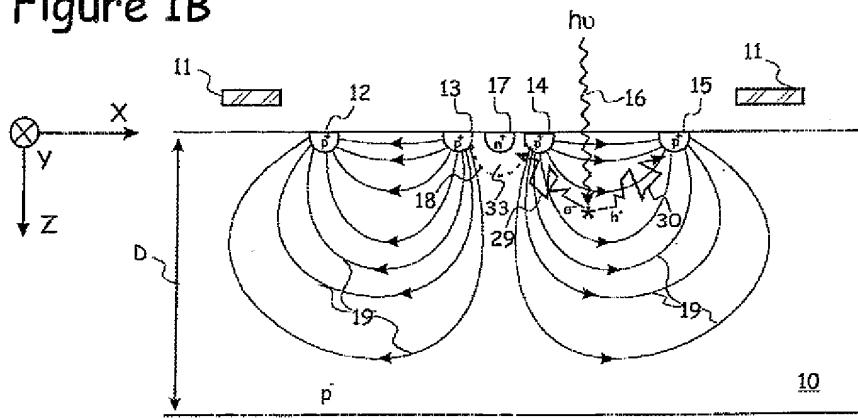
18. A method according to claim 17 furthermore comprising reading out the minority carriers ( $e^-$ ) collected in the detection region (17).

19. A method according to any of claims 16 to 18, furthermore comprising: generating the majority carrier current (19) so as to at least first generate the majority carrier current (19) in a first direction and to direct generated minority carriers towards a first detection region, and thereafter generate the majority carrier current (19) in a second direction, the second direction being different with respect to the first direction, to thereby direct generated minority carriers towards a second detection region.

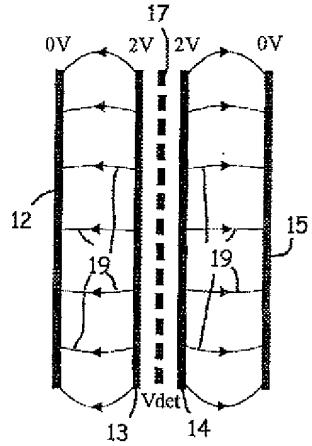
**Figure 1A**



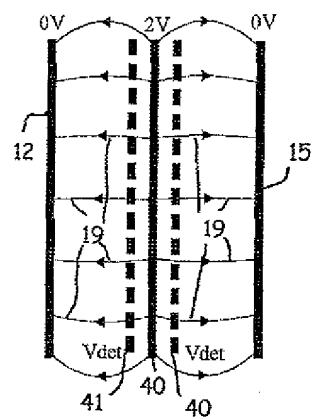
**Figure 1B**



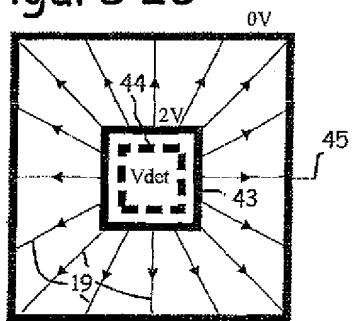
**Figure 2A**



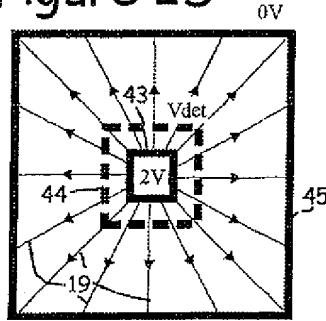
**Figure 2B**



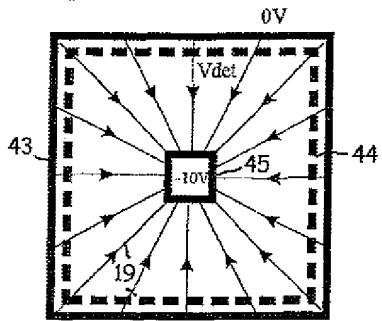
**Figure 2C**

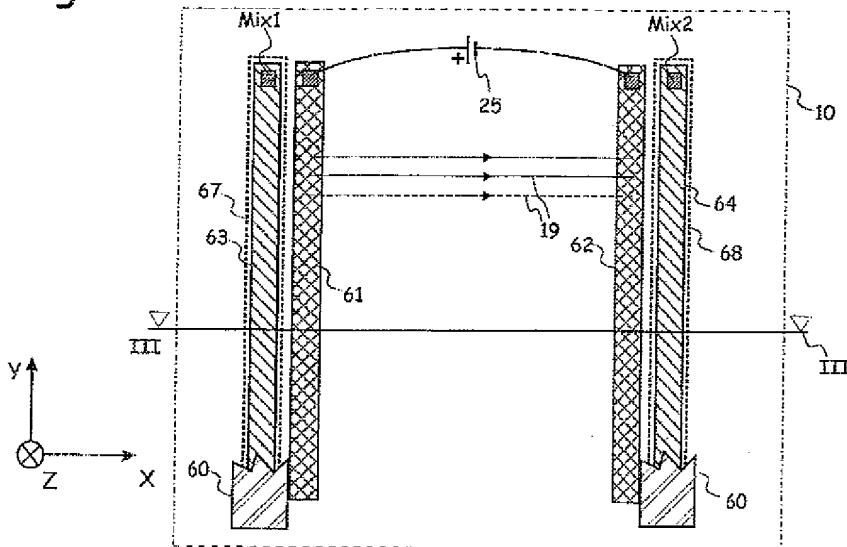
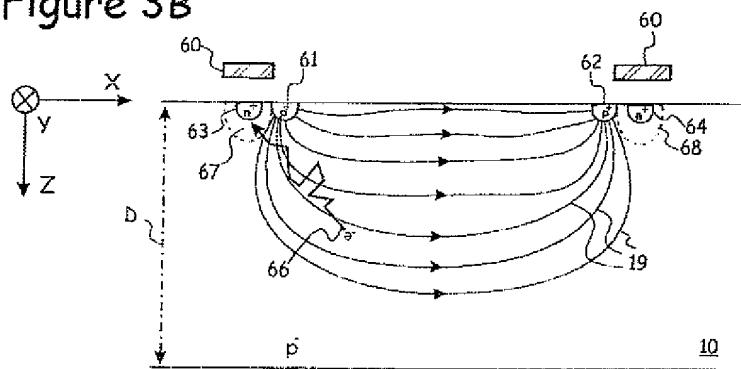
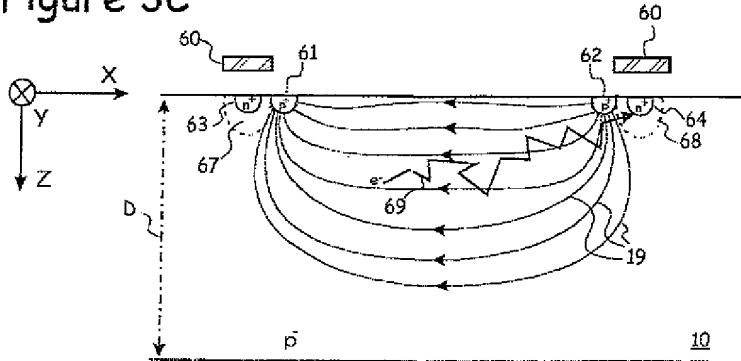


**Figure 2D**

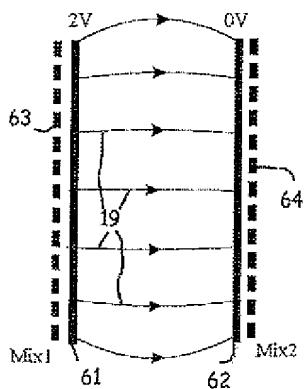


**Figure 2E**

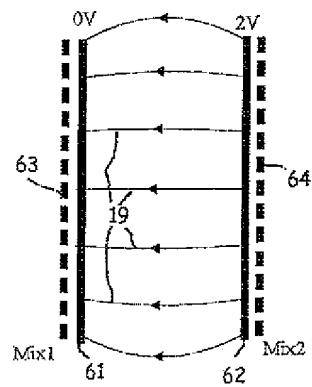


**Figure 3A****Figure 3B****Figure 3C**

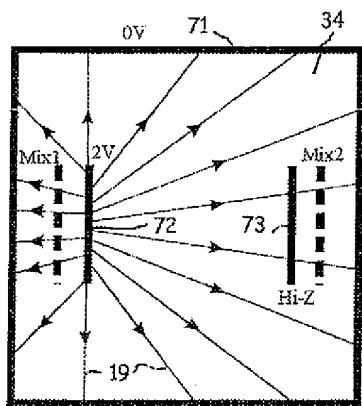
**Figure 4A**



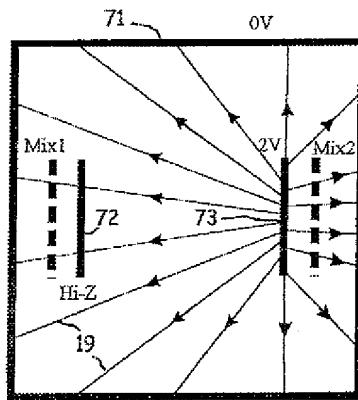
**Figure 4B**



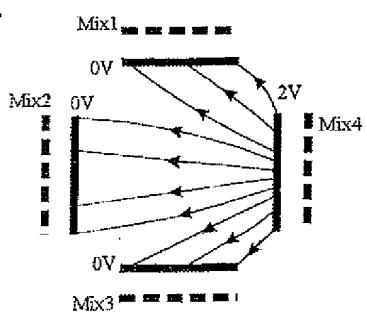
**Figure 4C**



**Figure 4D**



**Figure 4E**





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## EUROPEAN SEARCH REPORT

Application Number  
EP 03 07 7744

DOCUMENTS CONSIDERED TO BE RELEVANT							
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)				
A	US 2002/140045 AI (LAUTER ET AL) 3 October 2002 (2002-10-03) * paragraphs '0017!, '0018!, '0031!, '0039! * * figures 1,2 *	1-19	H01L31/0352 G01J1/42				
A	EP 0 883 187 A (IMEC INTER UNI MICRO ELECTR) 9 December 1998 (1998-12-09) * figure 1 * * claim 1 *	1-19					
A	WO 02 33817 A (SCHWARTE RUDOLF) 25 April 2002 (2002-04-25) * figure 6 *	1-19					
<table border="1"> <tr> <td colspan="2">TECHNICAL FIELDS SEARCHED (Int.Cl.7)</td> </tr> <tr> <td colspan="2">H01L G01J</td> </tr> </table>				TECHNICAL FIELDS SEARCHED (Int.Cl.7)		H01L G01J	
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<p>The present search report has been drawn up for all claims</p>							
1	Place of search	Date of completion of the search	Examiner				
	THE HAGUE	23 January 2004	Rasmussen, R				
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family corresponding document					
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23-01-2004

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